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DRR

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

ASAKAWA et al.

Art Unit: 2187

Application No. 09/816,236

Examiner: Y. Encarnacion

Filed: March 26, 2001

Atty. Docket No. 100021-00046

For: SYNCHRONOUS DRAM UTILIZABLE AS SHARED MEMORY

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
Washington, D.C. 20231

December 26, 2002

Sir:

In reply to the Office Action mailed July 2, 2002, the period for response being extended to January 2, 2003 by the attached Petition for Extension of Time, please amend the above-identified application as follows:

**RECEIVED**  
**DEC 30 2002**

**IN THE CLAIMS:**

**Technology Center 2100**

Please amend claim 2 as follows:

*A Sub B /*  
2. (Once Amended) A synchronous DRAM according to claim 1, wherein said plurality of memory blocks is constituted by continuous memory cells designated by addresses.

A copy of the marked up amended claim is attached to this response showing the changes as set forth in 37 C.F.R. § 1.121.

**REMARKS**

Claims 1-12 are pending in this application. By this Amendment, claim 2 is amended to correct a typographical error. No new matter is added.